

	Application No.	Applicant(s)
	дриошин но.	
Notice of Allowability	09/584,728	YOSHIMURA ET AL.
Nouce of Anowability	Examiner	Art Unit
	Khanh Tran	2631
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in the or other appropriate communication. This application is sub-	is application. If not included cation will be mailed in due course. THIS
1. This communication is responsive to <u>07/25/2005</u> .		
2. The allowed claim(s) is/are 4-15 and 18-20, which are ren	umbered as claims 1-15.	
3. The drawings filed on <u>06/01/2000</u> are accepted by the Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority ur</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>	e been received. e been received in Application I	No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment or in	the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s)		
1. Notice of References Cited (PTO-892)	5. Notice of Infor	mal Patent Application (PTO-152)
2.  Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🗌 Interview Sum	mary (PTO-413),
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Ma 08), 7. ☐ Examiner's Ar	Paper No./Mail Date 7.  Examiner's Amendment/Comment
Paper No./Mail Date  4.  Examiner's Comment Regarding Requirement for Deposit	8. ⊠ Examiner's St	atement of Reasons for Allowance
of Biological Material	9.	

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1. The Amendment After Final Rejection filed on 07/25/2005 has been entered.

Claims 4-15 and 18-20 are pending in this Office action.

- 2. Claims are renumbered as shown below:
- Claims 4-15 and 18-20 renumbered as claims 1-15.

## Response to Arguments

3. Applicant's arguments, see pages 9, filed on 07/25/2005, with respect to claims 4 and 14 have been fully considered and are persuasive. The rejection of claims 4 and 14 has been withdrawn. The pending application is now in condition for allowance after Applicants amend claims to include allowable features.

## Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

4. Regarding claim 4, claim is allowable because Applicants amended claim to include allowable limitations "said third latch circuit has a first output node for outputting a signal of an equal polarity to said data input signal and a second output node for outputting an inverted output of said signal output from said first output node". The

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closest prior art, Kobayashi et al. (US Patent 5,576,643) disclosing a data transfer circuit device, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

5. Regarding claim 5, said claim is directed to a digital synchronous circuit wherein the digital synchronous circuit has been amended to claim uniquely distinct features "wherein said control circuit includes a first pulse generating circuit for generating a first pulse signal according to a change in said input data signal" and "a third latch circuit for receiving said first pulse signal at a data input node and a clock input node" and "a level determination circuit for outputting a detection signal when potential of an output signal from said third latch circuit has crossed a reference potential" and "a second pulse generating circuit for generating a second pulse signal according to a change in potential of said detection signal and outputting said second pulse signal as said control signal". The closest prior art, Kobayashi et al. (US Patent 5,576,643) disclosing a data transfer circuit device, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Conclusion<sup>®</sup>

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Tran whose telephone number is 571-272-3007. The examiner can normally be reached on Monday - Friday from 08:00 AM - 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on 571-272-3021. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

**KCT** 

MOHAMMED GHÁYOUR SUPERVISORY PATENT EXAMINER